

B 50564

207	315
Class	Subclass
ISSUE CLASSIFICATION	

PATENT NUMBER

<p><b>O.I.P.E.</b></p> <p><b>SCANNED</b> <u>TR4</u> <b>Q.A.</b> <u>Am</u></p>	<p><b>PATENT DATE</b></p>
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APPLICATION NO. 09/945554	CONT/PRIOR	CLASS <del>438</del> 257	SUBCLASS 315	ART UNIT <del>2812</del> 2826	EXAMINER
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TITLE	APPLICANTS
1. <b>Chairman</b>	1. <b>Mr. J. H. ...</b>
2. <b>Vice Chairman</b>	2. <b>Mr. J. H. ...</b>
3. <b>Secretary</b>	3. <b>Mr. J. H. ...</b>
4. <b>Treasurer</b>	4. <b>Mr. J. H. ...</b>
5. <b>Member</b>	5. <b>Mr. J. H. ...</b>
6. <b>Member</b>	6. <b>Mr. J. H. ...</b>
7. <b>Member</b>	7. <b>Mr. J. H. ...</b>
8. <b>Member</b>	8. <b>Mr. J. H. ...</b>
9. <b>Member</b>	9. <b>Mr. J. H. ...</b>
10. <b>Member</b>	10. <b>Mr. J. H. ...</b>
11. <b>Member</b>	11. <b>Mr. J. H. ...</b>
12. <b>Member</b>	12. <b>Mr. J. H. ...</b>
13. <b>Member</b>	13. <b>Mr. J. H. ...</b>
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92. <b>Member</b>	92. <b>Mr. J. H. ...</b>
93. <b>Member</b>	93. <b>Mr. J. H. ...</b>
94. <b>Member</b>	94. <b>Mr. J. H. ...</b>
95. <b>Member</b>	95. <b>Mr. J.</b>

Leonard Forbes

DRAM cells with repressed floating gate memory, low tunnel barrier-interpoly insulators

PTO-2040  
12/99

ISSUING CLASSIFICATION									
ORIGINAL				CROSS REFERENCE(S)					
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
257		315		263	185-08				
INTERNATIONAL CLASSIFICATION				257	903				
F	I	C	1400						
F	I	C	1112						

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	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____  _____  _____	_____ (Primary Examiner) (Date)		<b>ISSUE FEE</b>	
			Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)		<b>ISSUE BATCH NUMBER</b>	
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